

REMARKS

Claims 1-9 and 12-16 have been rejected under 35 USC 102(a) as anticipated by JP Application No. 63178808 (the '808 reference). The rejection is respectfully traversed.

Claim 1 has been amended to incorporate a majority of the limitations of claims 3, 12, 13 and 15 (now canceled). The Examiner cites the '808 reference as disclosing (with reference to claims 12-13 and 15 on page 4 of the Office Action) a deposition of metal being a plating process, and that it "is inherent that the plating process is a galvanizing process and a galvanizing potential is generated at the pin [sic] by a corresponding voltage being applied at a further pin and/or side web connected with the pin internals via the corresponding semiconductor device." Applicant's respectfully disagree.

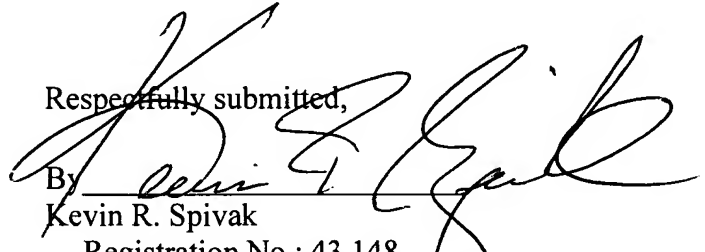
The claimed invention requires that during the deposition process a galvanizing potential is generated at the pin (4e) by a corresponding voltage being applied at a further pin (4k) of the semiconductor device and/or side web. Additionally, it is electrically connected with the pin (4e) internally via the semiconductor device (3a). The '808 reference fails to disclose a "further pin" that is electrically connected with the [first] pin internally via the semiconductor device. Rather, the '808 reference discloses simply that after cutting, metallic plating is applied to the external connection lead 12. While it may be true that a galvanizing process and potential is generated, there is no disclosure that this is accomplished by virtue of a further pin, with the pin electrically connected with the pin internally via the semiconductor device. This feature is clearly not inherent to the '808 reference. Applicant's respectfully request that the Examiner cite where in the '808 reference support is found, or withdraw the rejection of record.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to withdraw the outstanding rejection of the claims and to pass this application to issue. If it is determined that a telephone conference would expedite the prosecution of this application, the Examiner is invited to telephone the undersigned at the number given below.

In the event the U.S. Patent and Trademark office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 03-1952 referencing docket no. 543822003900. However, the Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: November 15, 2005

Respectfully submitted,

By 
Kevin R. Spivak
Registration No.: 43,148
MORRISON & FOERSTER LLP
1650 Tysons Blvd, Suite 300
McLean, Virginia 22102
(703) 760-7762